

1/8

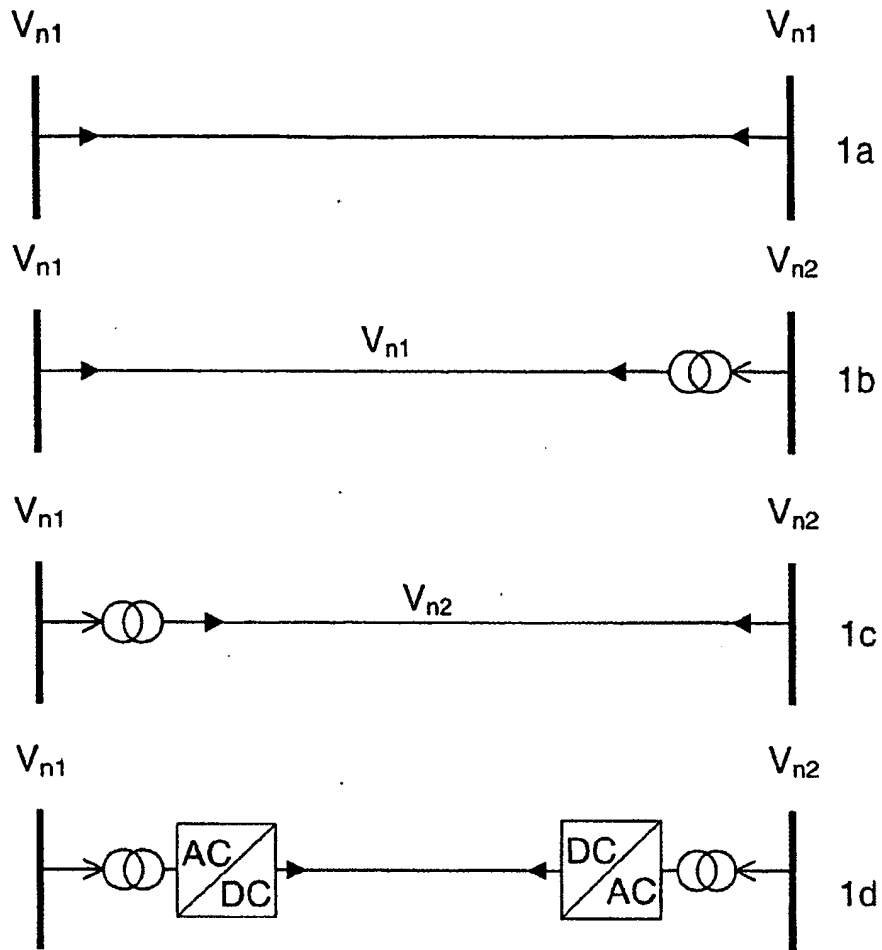


Figure 1a-d Prior Art

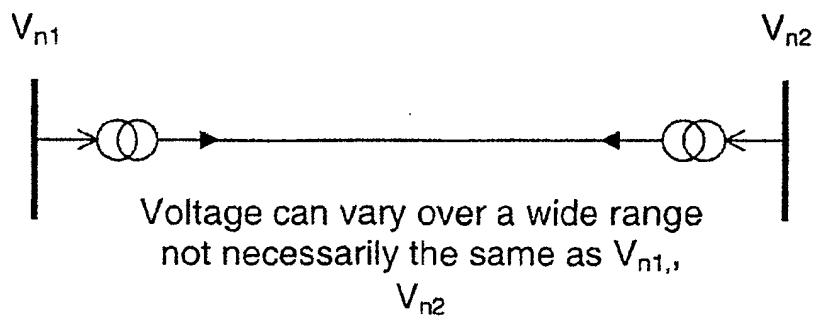


Figure 2

2/8

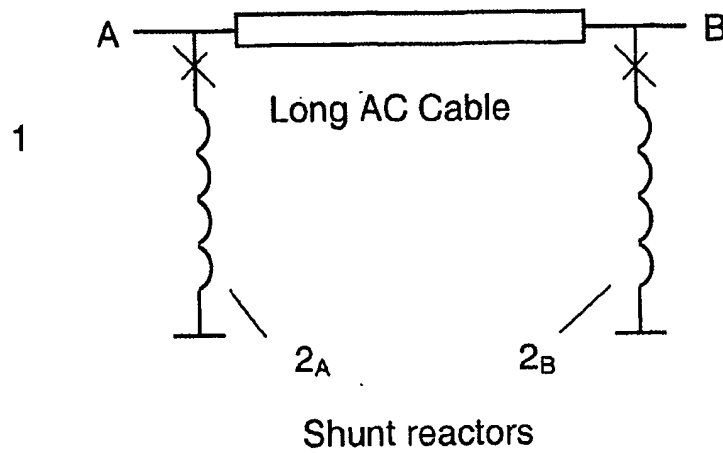


Figure 3 Prior Art

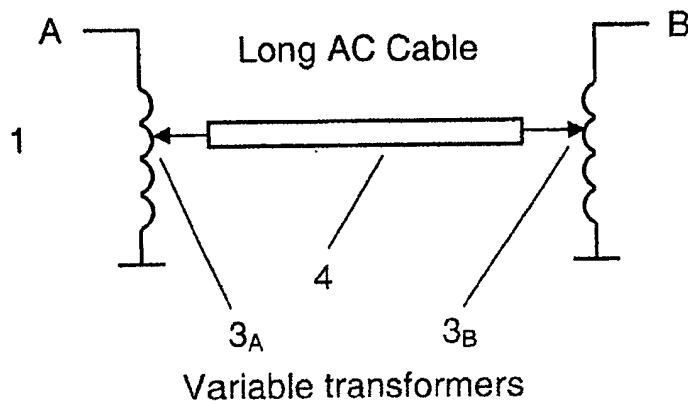


Figure 4

3/8

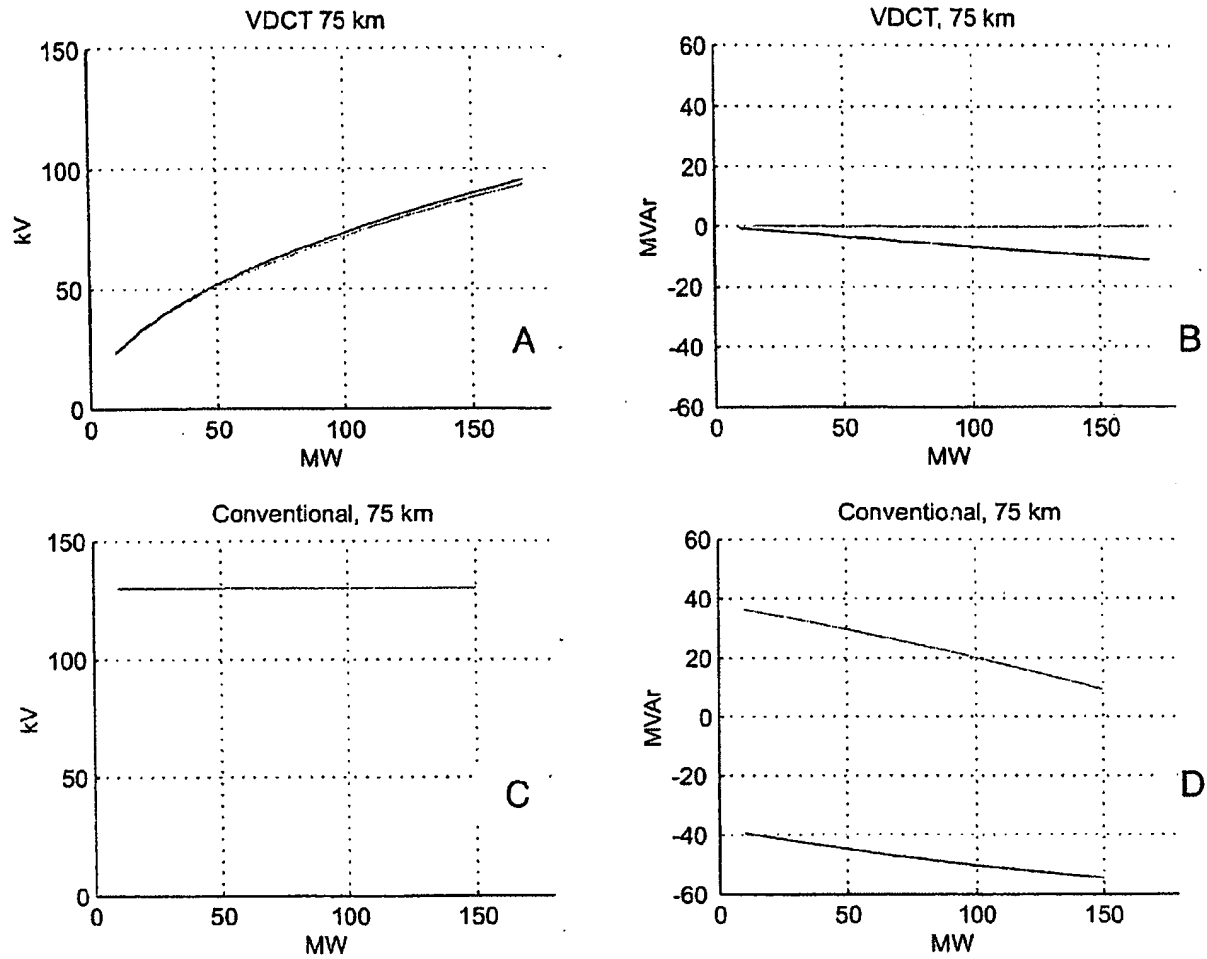


Figure 5

4/8

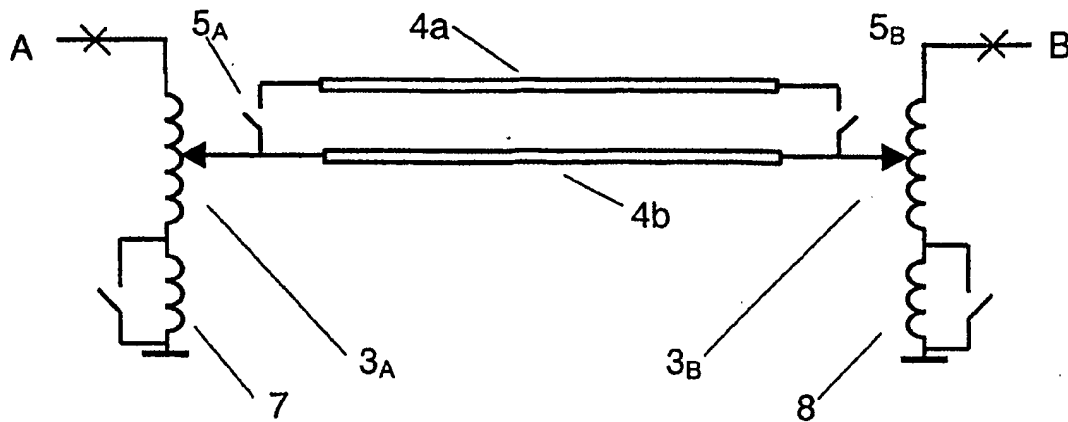


Figure 6

### Cable Voltage Optimization

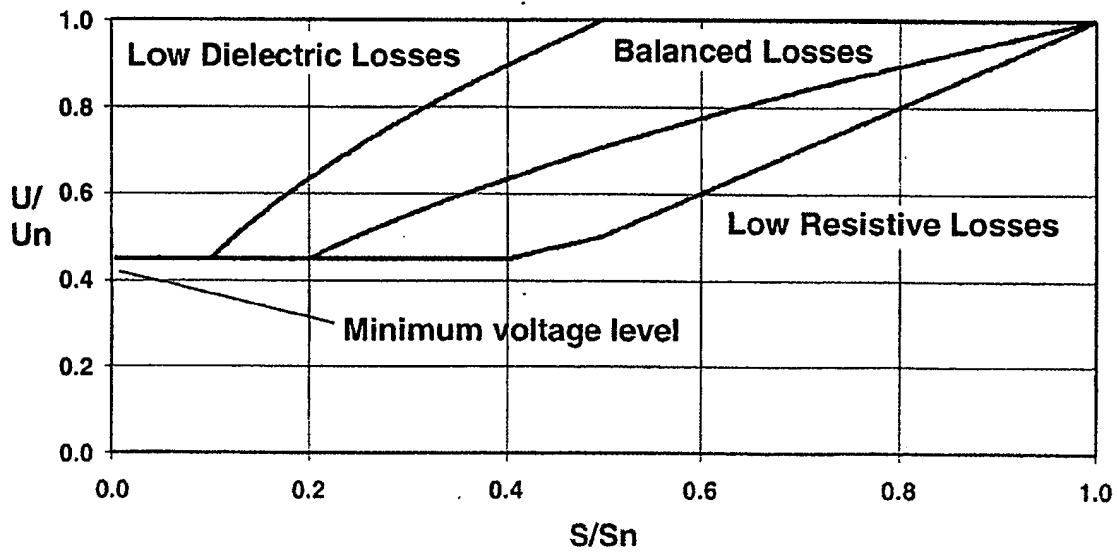


Figure 7

5/8

### Cable Voltage Optimization

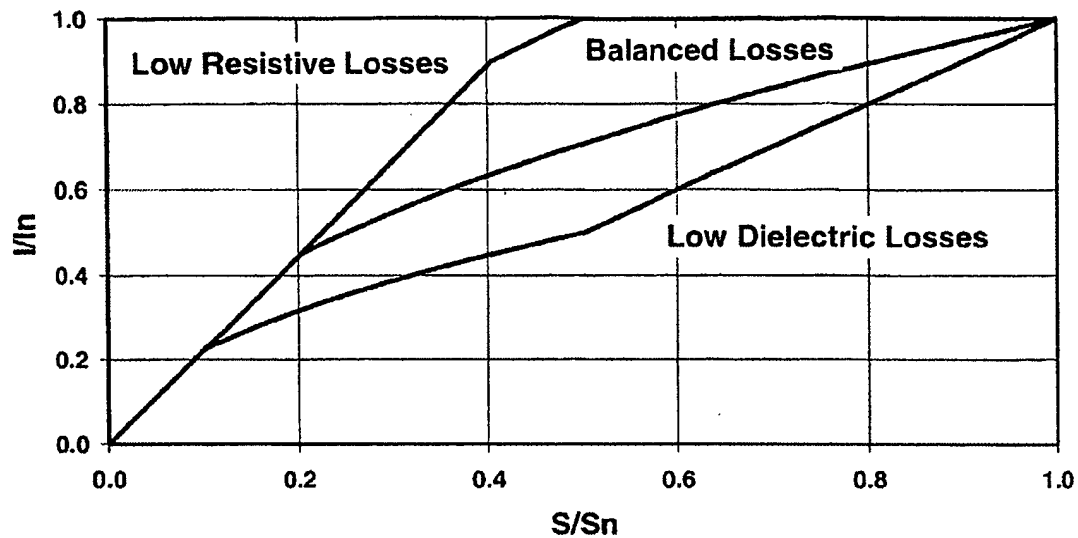


Figure 8

### Cable Voltage Optimization

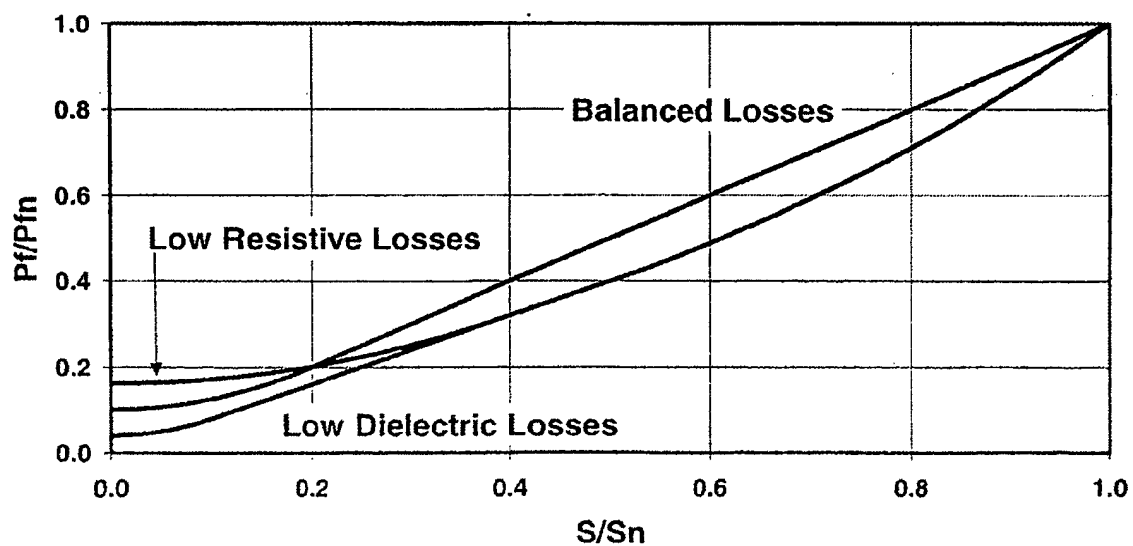


Figure 9

6/8

### Low Dielectric Losses

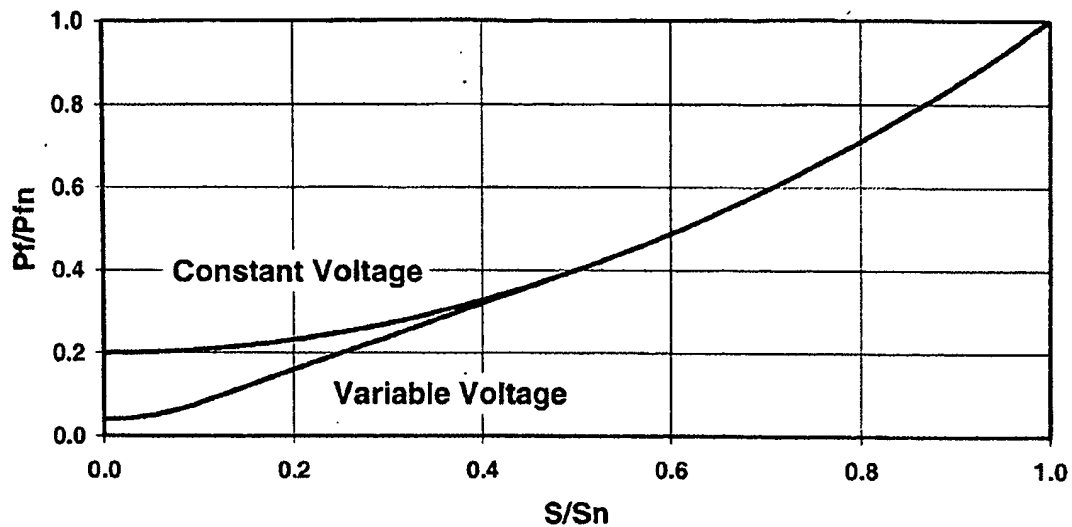


Figure 10

### Balanced Losses

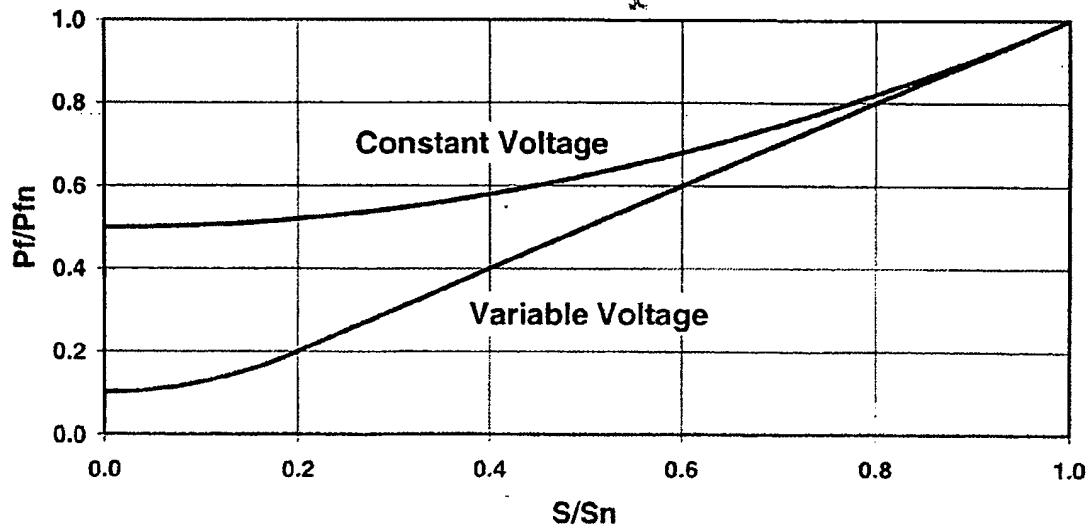


Figure 11

7/8

### High Dielectric Losses

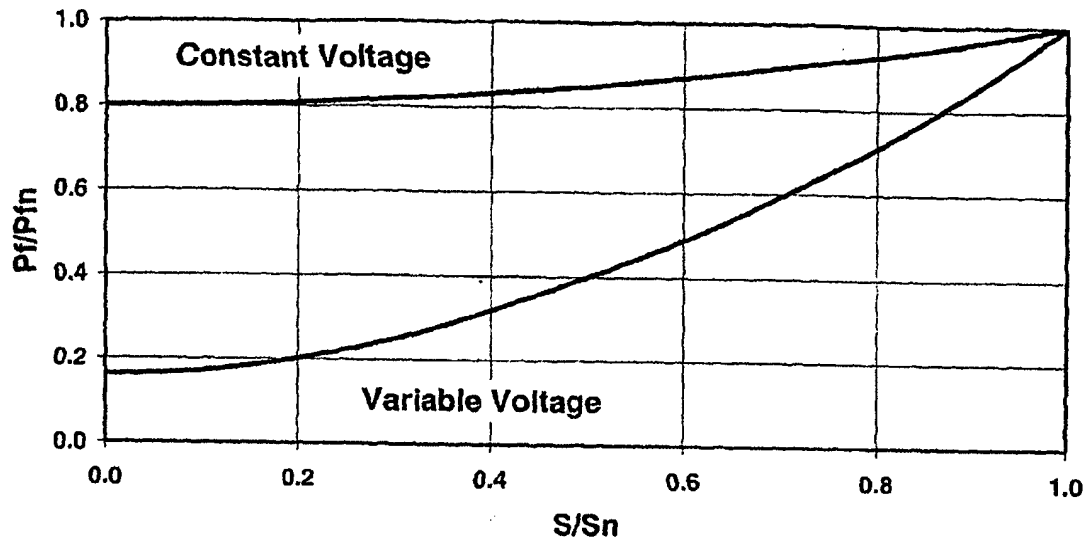


Figure 12

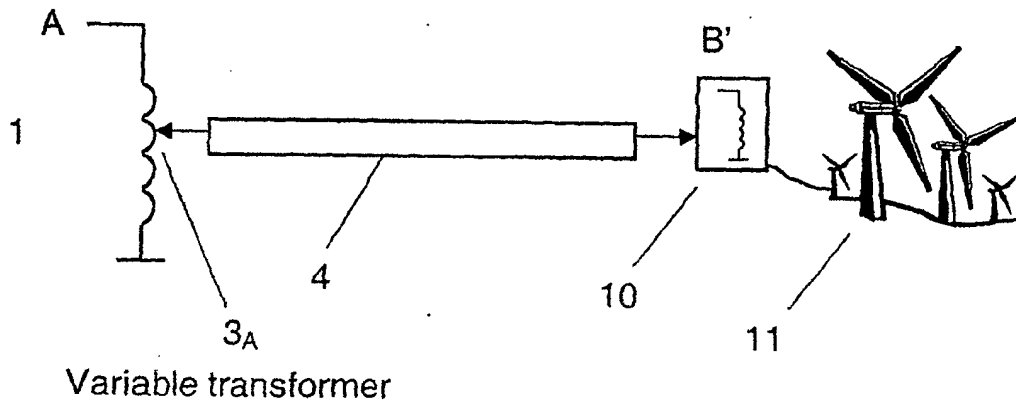
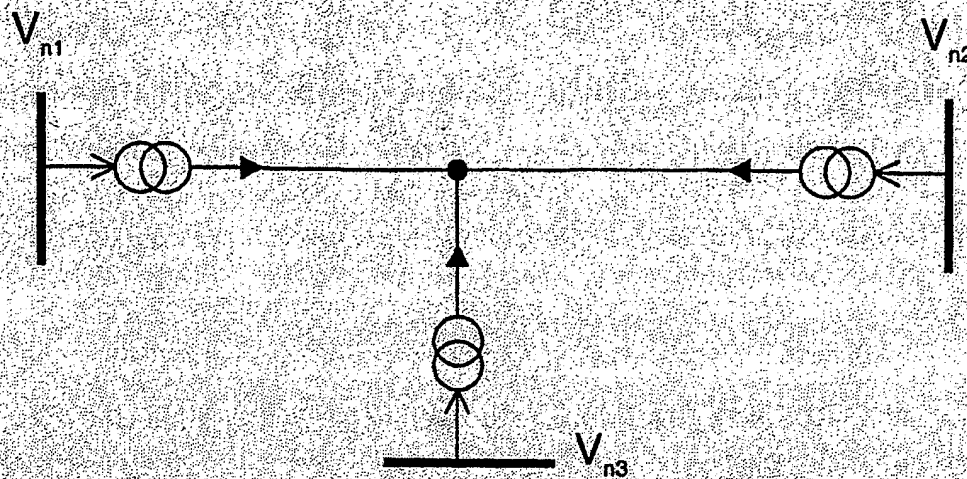


Figure 13



### Figure 14

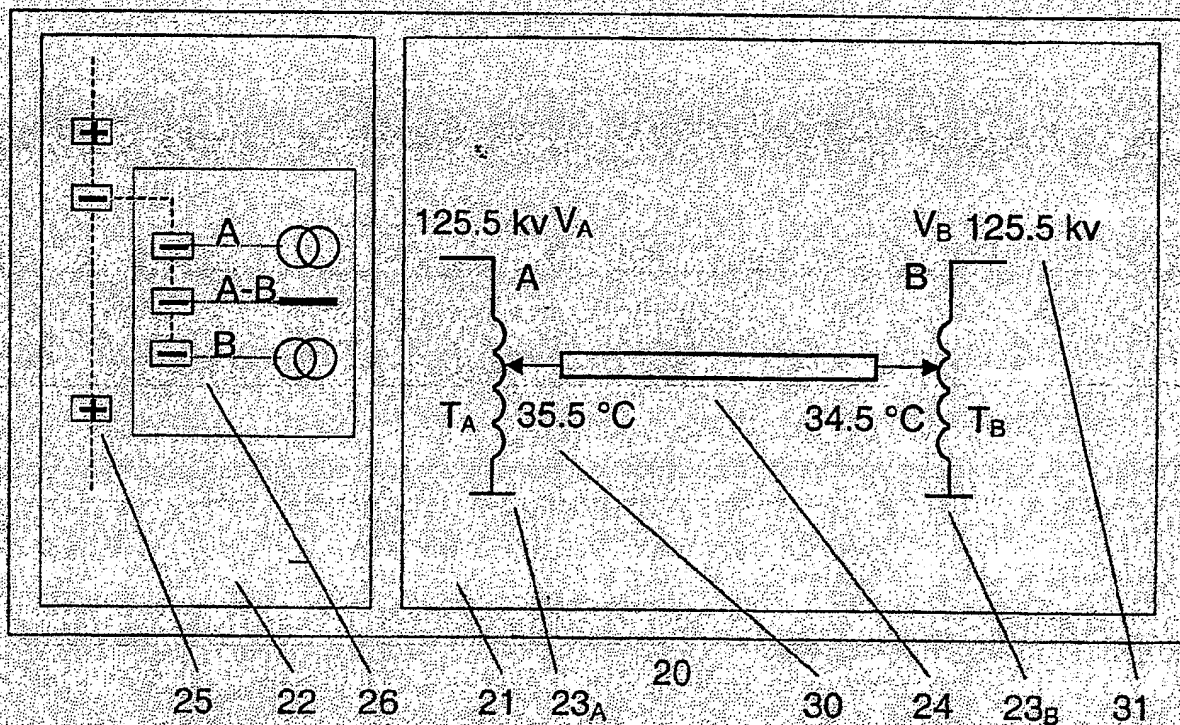


Figure 15